

ABSTRACT OF THE DISCLOSURE

5 A jitter detection apparatus includes an A/D
conversion section for converting an input analog signal
into a plurality of discrete multiple value digital signals;
a binarization section for performing binarization of the
plurality of multiple value digital signals to generate a
binary signal; a jitter calculation section for calculating
10 a jitter amount based on an error between a value of a
prescribed multiple value digital signal sampled at a time
which is substantially the same as a time when the value
of the binary signal is changed and a prescribed threshold
value; a pattern detection section for detecting patterns
15 of the binary signal before and after the time when the
prescribed multiple value digital signal is sampled; and
a correction section for correcting the jitter amount based
on the detected pattern.